

## EET201 / EET205 / EET212 Data Sheets

<i><b>PART</b></i>	<i><b>DESCRIPTION</b></i>
<a href="#">2n3904</a>	NPN General Purpose Amplifier
<a href="#">ADC0801</a>	8-Bit $\mu$ P Compatible A/D Converter
<a href="#">LM135</a>	Precision Temperature Sensors
<a href="#">PN 1124</a>	Phidgets temperature sensor board
<a href="#">4N28</a>	Optocoupler
<a href="#">MDPXX</a>	Resistor Networks, Dual-In-Line, Molded DIP
<a href="#">SA56</a>	SINGLE DIGIT NUMERIC DISPLAYS
<a href="#">SCL4543B</a>	BCD-To-Seven Segment latch / decoder / driver
<a href="#">MAX6373</a>	Maxim programmable watchdog chip
<a href="#">TIP120</a>	NPN Power Transistor
<a href="#">TIP125</a>	PNP Power Transistor
<a href="#">24AA025T</a>	I <sup>2</sup> C Memory Chip
<a href="#">7400</a>	Quad 2-Input <b>NAND</b>
<a href="#">7401</a>	Quad 2-input <b>NAND</b> with Open-Collector Outputs
<a href="#">7402</a>	Quad 2-input <b>NOR</b>
<a href="#">7403</a>	Quad 2-input <b>NAND</b> with Open-Collector Outputs
<a href="#">7404</a>	Hex <b>INVERTER</b>
<a href="#">7405</a>	Hex <b>INVERTER</b> with Open Collector Outputs
<a href="#">7406</a>	Hex <b>BUFFER</b> with Open-Collector High-Voltage Outputs
<a href="#">7408</a>	Quad 2-input <b>AND</b>
<a href="#">7410</a>	Triple 3-input <b>NAND</b>
<a href="#">7411</a>	Triple 3-input <b>AND</b>
<a href="#">7413</a>	Dual 4-input <b>NAND</b> Schmitt Triggers
<a href="#">7414</a>	Hex <b>INVERTER</b> Schmitt Triggers
<a href="#">7420</a>	Dual 4-input <b>NAND</b>
<a href="#">7430</a>	8-input <b>NAND</b>
<a href="#">7432</a>	Quad 2-input <b>OR</b>
<a href="#">7440</a>	Dual 4-input <b>NAND</b> buffer
<a href="#">7442</a>	BCD to Decimal <b>DECODER</b>
<a href="#">7447</a>	BCD to 7-Segment <b>DECODER/DRIVER</b>
<a href="#">7451</a>	Dual 2-Wide, 2-input <b>AND-OR-INVERT</b>
<a href="#">7455</a>	2-Wide, 4-input <b>AND-OR-INVERT</b>

<a href="#">7474</a>	Dual D Positive-Edge-Triggered <b>FLIP-FLOP</b> with Preset and Clear
<a href="#">7475</a>	Quad <b>LATCH</b>
<a href="#">7476</a>	Dual J-K <b>FLIP FLOP</b> with Preset and Clear
<a href="#">7482</a>	2- bit Binary <b>ADDER</b> with Carry
<a href="#">7483</a>	4-bit Binary <b>ADDER</b> with Fast Carry
<a href="#">7485</a>	4-bit Magnitude <b>COMPARATOR</b>
<a href="#">7486</a>	Quad 2-input <b>XOR</b>
<a href="#">7489</a>	64-bit Read/Write <b>MEMORY</b>
<a href="#">7490</a>	Decade <b>COUNTER</b>
<a href="#">7491</a>	8-bit Serial <b>SHIFT REGISTER</b>
<a href="#">7493</a>	4-bit Binary <b>COUNTER</b>
<a href="#">7495</a>	4-bit Right/Left <b>SHIFT REGISTER</b>
<a href="#">74121</a>	Non-Retriggerable Monostable <b>MULTIVIBRATOR</b>
<a href="#">74122</a>	Retriggerable Monostable <b>MULTIVIBRATOR</b>
<a href="#">74123</a>	Dual Retriggerable Monostable <b>MULTIVIBRATOR</b>
<a href="#">74125</a>	Quat TRI-STATE <b>BUFFER</b>
<a href="#">74132</a>	Quad 2-input Schmitt Trigger <b>NAND</b>
<a href="#">74138</a>	3-to-8 Line <b>DECODER/DEMULTIPLEXER</b>
<a href="#">74147</a>	Priority <b>ENCODER</b>
<a href="#">74148</a>	Priority <b>ENCODER</b>
<a href="#">74151</a>	1-of-8 Line <b>DECODER/DEMULTIPLEXER</b>
<a href="#">74153</a>	Dual 1-of-4 Line Data Selector/ <b>MULTIPLEXER</b>
<a href="#">74155</a>	Dual 2-Line to 4-Line <b>DECODER/DEMULTIPLEXER</b>
<a href="#">74157</a>	Quad 2-to-1 Line Data Selector/ <b>MULTIPLEXER</b>
<a href="#">74162</a>	Synchronous Presettable BCD Decade <b>COUNTER</b>
<a href="#">74163</a>	Synchronous 4-bit Binary <b>COUNTER</b> with Synchronous Clear
<a href="#">74164</a>	8-bit Serial In/Parallel Out <b>SHIFT REGISTER</b> with Asynchronous Clear
<a href="#">74165</a>	8-bit Parallel in/Serial Out Shift Register
<a href="#">74173</a>	4-bit TRI-STATE D <b>REGISTER</b>
<a href="#">74175</a>	Quad D <b>FLIP-FLOP</b> with Clear Complementary Outputs
<a href="#">74191</a>	Synchronous 4-bit Up/Down Binary <b>COUNTER</b> with Mode Control
<a href="#">74192</a>	Up/Down Decade <b>COUNTER</b> with Separate UP/DOWN Clocks
<a href="#">74193</a>	Synchronous 4-Bit Up/Down Binary <b>COUNTER</b> with Dual Clock
<a href="#">74194</a>	4-bit Bidirectional Universal <b>SHIFT REGISTER</b>
<a href="#">74244</a>	Octal TRI-STATE <b>BUFFER</b> /Line Driver/Line Receiver

<a href="#">74251</a>	TRI-STATE 1-of-8 Line Data Selector/ <b>MULTIPLEXER</b> with Complementary Outputs
<a href="#">74257</a>	Quad TRI-STATE 2-to-1 Line Data Selector/ <b>MULTIPLEXER</b>
<a href="#">74259</a>	8-bit Serial In to Parallel Out Addressable <b>LATCH</b>
<a href="#">74279</a>	Quad /S-/R <b>LATCH</b>
<a href="#">74365</a>	Hex TRI-STATE <b>BUFFER</b> /Bus Driver
<a href="#">74366</a>	Hex TRI-STATE Inverting <b>BUFFER</b> /Bus Driver
<a href="#">74373</a>	Octal TRI-STATE Transparent D <b>LATCH</b>
<a href="#">74540</a>	Octal <b>BUFFER</b> /Line Driver with TRI-STATE Outputs